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IAP5 Rec'd PCT/PTO 28 MAR 2006  
PATENT

Docket No.: 13596/3

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS: Hidefumi TAKAYA, et al.  
SERIAL NO. : (Natl. Phase of PCT/JP2004/015179)  
FILED : (Herewith)  
FOR : INSULATED GATE TYPE SEMICONDUCTOR  
DEVICE AND MANUFACTURING METHOD  
THEREOF  
GROUP ART UNIT : (Unassigned)  
Examiner: (Unassigned)

COMMISSIONER FOR PATENTS  
Customer Service Window  
Randolph Bldg.  
401 Dulany Street  
Alexandria, VA 22314

**PRELIMINARY AMENDMENT**

SIR:

Prior to examination of this national phase application, please enter the following amendment.

Amendments to the specification begin on page 2. Amendments to the claims begin on page 3. Remarks begin on page 10.